U.S. Department of Commerce, Patent and Trademark Office						Attorney Docket No.:	
					CIS0294US		10/666,306
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s):		Confirmation No.
(Use several sheets if necessary)					Smith et al.		7927
					Filing Date:		Group Art Unit:
					September 18, 2003		2419
			U.S. Pate	nt Documents			
Examiner		Document					Filing Date
Initial	<u> </u>	Number	Date	Name	Class	Subclass	lf Appropriate
	AA	4,387,371	June 1983	Beker et al.	340	825.52	
	AB	5,959,972	Sept. 1999	Hamami	370	228	
	AC	6,195,351	Feb. 2001	Hiscock et al.	370	389	
	AD	6,222,820	April 2001	Hamami	370	218	
	AE	6,229,787	May 2001	Byrne	370	218	
	AF	6,275,953	Aug. 2001	Vahalia et al.	G06F	11/16	
	AG	6,377,992	April 2002	Fernandez et al.	G06F	15/16	
	AH	6,388,995	May 2002	Gai et al.	370	256	
	AI	6,460,088	Oct. 2002	Merchant	709	236	
	AJ	6,535,490	March 2003	Jain	370	256	
	AK	6,738,345	May 2004	Williamson	370	217	
	AL	7,127,633	Oct. 2006	Olson et al.	714	4	·
			Patent Applie	cation Publication	on	•	
		Publication Number	Pub. Date	Name	Class	Subclass	Filing Date If Appropriate
	AM	2002/0018489	Feb. 2002	Ambe et al.	370	475	
	AN	2002/0186654	Dec. 2002	Tornar	370	225	
	AO	2002/0188711	Dec. 2002	Meyer et al.	709	223	
	AP	2003/0007489	Jan. 2003	Krishnan et al.	370	392	
	AQ	2004/0105390	June 2004	Saksio	370	245	1
	AR	2005/0169311	Aug. 2005	Miller et al.	370	471	_
	AS	US2005/0243826	Nov. 2005	Smith et al.	370	392	1
	AT	2007/0159971	July 2007	Zhang et al.	370	230	1
	1	·		or, Title, Date, Peri			<u>l</u>
-	AU	Cisco Systems, Inc.; C	Opyright 1989-1997	7 ©			
		http://www.cisco.com/univered/cc/td/doc/product/rtrmgmt/sw_ntman/cwsimain/cwsi2/cwsiug2/ylan2/stpanp.htm					
	300000000000000000000000000000000000000	"Understanding Spanning-Tree Protocol," Appendix C, pages C-1 through C-12.					
	AV	PEDROSO, Carlos Marcelo and FONSECA, Keiko, Modeling Weight Round Robin Packet Scheduler With Petri Nets, Communication Systems, IEEE, vol. 1, November 25, 2002, pages 342-345.					
	AW	LIU, Dake, et al, Configuration-Based Architecture for High Speed and General-Purpose Protocol Processing, Signal Processing Systems, IEEE, October 20, 1999, pages 540-547.					
Examiner /Brian Roberts/					Date Considered: 08/26/2009		
		f reference considered	1.4				